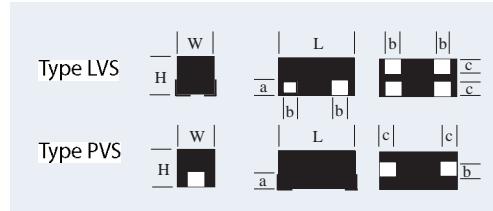


# LVS/PVS - CURRENT SENSING

## How LVS/PVS Shunts Will Benefit You:

SMD current-sensing ..... to 15 amperes  
 Ohmic/voltage drop tolerances ..... to  $\pm 0.005\%$   
 Values ..... from 1 miliohm to 100K $\Omega$   
 TCR Char. .... 15ppm(Std.) to  $0 \pm 10 \text{ppm}/^\circ\text{C}$   
 Temperature Span .....  $-65$  to  $+275^\circ\text{C}$  (@ 1%)  
 (For closer tolerance see Derating Curve (Fig. 3))



## ELECTRICAL & PHYSICAL SPECIFICATIONS

PRC TYPE	Max. Watt Amp	PAD LAYOUT 	DIMENSIONS $\pm .787 \text{ MM } (.031\text{'})$							Max. Res. ( $\Omega$ )	Std. Min. Res. @ Max. Watts Special Min. Res. @ Derated Watts	
			H mm ins.	L mm ins.	W mm ins.	a mm ins.	b mm ins.	c mm ins.	d mm ins.			e mm ins.
PVS1	1W 3A		3.30 .130"	9.14 .360"	3.18 .125"	1.91 .075"	1.91 .075"	2.54 .100"	6.60 .260"	5K	.111 $\Omega$ @ 1W .001 $\Omega$ @ .009 W	
LVS2	2W 8A		6.35 .250"	9.78 .385"	5.72 .225"	3.18 .125"	2.84 .112"	2.54 .100"	4.90 .193"	3.81 .150"	100	.03 $\Omega$ @ 2W
PVS2									7.87 .310"	15K	.001 $\Omega$ @ 0.064W	
LVS3	3W 15A		6.35 .250"	12.7 .500"	6.35 .250"	2.54 .100"	2.84 .112"	2.54 .100"	6.99 .275"	4.70 .185"	100	.013 $\Omega$ @ 3W
PVS3									10.8 .425"	50K	.001 $\Omega$ @ 0.225W	
LVS5	5W 15A		7.87 .310"	15.88 .625"	7.87 .310"	1.91 .075"	2.84 .112"	2.54 .100"	8.08 .318"	6.10 .240"	100	.022 $\Omega$ @ 5W
PVS5									14.0 .551"	100K	.001 $\Omega$ @ 0.22W	

### ENGINEERING DATA:

#### 1. RESISTANCE AND TOLERANCES

You can select any ohmic value or decimal part of an ohm from 0.001 $\Omega$  to 100K $\Omega$  with microhm/microvolt accuracies to  $\pm 0.005\%$  see Fig. 2, p. 7. PRC's unique "single-joint" design on the four (4) terminal parts makes tab identification academic.

#### 2. TCR CHARACTERISTICS

$0 \pm 15 \text{ppm}/^\circ\text{C}$  (std.) Please specify temperature span of operation. Add LTC in the part # for TCR  $0 \pm 10 \text{ppm}/^\circ\text{C}$  to  $+150^\circ\text{C}$ .

#### 3. STABILITY VS. TIME

to  $\pm 0.001\%$ /yr. at  $25^\circ\text{C}$ . (no load)

#### 4. PRECISION POWER

Standard Min. Res. @ Max. Watts based upon  $\pm 1\%$  resistance tolerances at  $25^\circ\text{C}$ . (please see end column above). Derating is required for higher temperatures, closer tolerances and lower resistance values - please see Fig. # 3, on page 7.

#### 5. PROTECTIVE SEAL

Rectangular solvent-resistant epoxy case offers excellent thermal transfer to base.

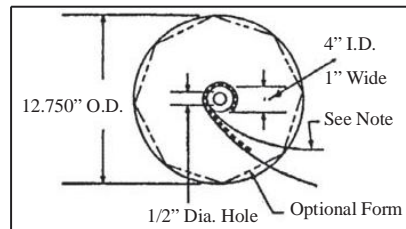
#### 6. TERMINALS

Solderable "hot-tinned" pure copper (ETP/OFHC) tab terminals and low EMP construction reduces thermal effects usually associated with low value resistors.

#### 7. SMT "Carrier Tape" PACKAGING

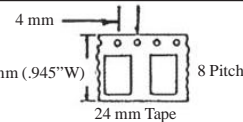
per IEC 286-3 (EIA 481):

Please see Purchasing Information on pg 3.

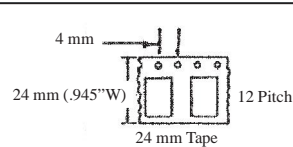


Note: Skin packed to tape with polyfilm

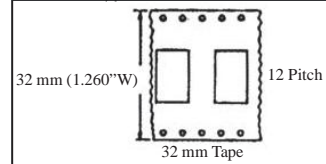
For types: HV51, PRC100T, PTS1, PVS1 & ST35



For types: HV52, HV53, PTS2, PTS3, LVS3, PVS3



For types: LVS5, PVS5 & HV55



ISSUE NO. 42

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